

DIGITAL LOGIC DESIGN

Lab Experiments

EXPERIMENT: 1

AIM:

Introduction to digital electronics lab- nomenclature of digital ICs, specifications, study of the data sheet, concept of Vcc and ground, verification of the truth tables of logic gates using TTL ICs.

Apparatus Required :-

Digital lab kit, single strand wires, breadboard, TTL IC'S.

Gates	IC NO.
AND	7408
OR	7432
NAND	7402
NOR	7400
NOT	7404
XOR	74136

Theory:-

Logic gates are idealized or physical devices implementing a Boolean function, which it performs a logical operation on one or more logical inputs and produce a single output. Depending on the context, the term may refer to an ideal logic gate, one that has for instance zero rise time and unlimited fan out or it may refer to a non-ideal physical device.

The main hierarchy is as follows:-

1. Basic Gates
2. Universal Gates
3. Advanced Gate

Experiments In Logic And Computer Design

WJ Hussar



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